Appl. No. 10/695,187 Amdt. Dated March 20, 2006 Reply to Office Action of January 18, 2006 Docket No. IS01207AP Customer No. 22917

## Amendments to the Claims:

1. (previously presented) A method for vertical removal of excess solder from a circuit substrate, the method comprising the steps of:

providing a sacrificial circuit substrate with a plurality of pads, a portion of each pad having a solder-wettable material disposed thereon and wherein the plurality of pads are connected to vias comprising through-holes that are plated with a solder-wettable material;

placing the plurality of pads of the sacrificial circuit substrate in vertical proximity to the excess solder of the circuit substrate;

heating the excess solder to a liquidous state;

wicking the excess solder vertically onto the pads of the sacrificial circuit substrate by capillary action; and

lifting the sacrificial circuit substrate from the proximity of the circuit substrate while the solder is in a liquidous state.

- 2. (original) The method of claim 1, wherein the circuit substrate is flexible and wherein the providing step includes providing a rigid sacrificial circuit substrate.
- 3. (original) The method of claim 1, wherein the heating step includes using hot gas to reflow the excess solder.
- 4. (withdrawn) The method of claim 1, wherein the providing step include providing solder-wettable vias connected to the pads, the vias including through-holes that are plated with a solder-wettable material.
- 5. (previously presented) The method of claim 41, wherein the wicking step includes wicking of the solder into the through-holes of the vias.

Appl. No. 10/695,187 Amdt. Dated March 20, 2006 Reply to Office Action of January 18, 2006 Docket No. IS01207AP Customer No. 22917

- 6. (previously presented) The method of claim 41, wherein the dimensions of the pads, vias and through-holes are configured to leave a residual amount of solder on the circuit substrate after the wicking and lifting steps.
- 7. (previously presented) The method of claim 41, wherein the wicking step further comprises applying a vacuum to the vias to assist wicking of the solder into the vias.
- 8. (original) The method of claim 1, further comprising the step of applying flux to the sacrificial circuit substrate.
- 9. (previously presented) A method for vertical removal of excess solder from a ball-grid array portion of a circuit substrate having a solder mask disposed thereon, the method comprising the steps of:
  - providing a rigid sacrificial circuit substrate with a plurality of vias arranged in an array pattern matching that of the ball-grid array portion of the circuit substrate, the vias having solder-wettable through-holes;
  - placing the plurality of vias of the sacrificial circuit substrate in vertical proximity to the excess solder of the ball-grid array portion of the circuit substrate;

heating the excess solder to a liquidous state;

- wicking the excess solder vertically into the vias of the sacrificial circuit substrate by capillary action; and
- lifting the sacrificial circuit substrate from the proximity of the circuit substrate while the solder is in a liquidous state to prevent lateral movement across the solder mask.
- 10. (original) The method of claim 9, wherein the heating step includes using hot gas to reflow the excess solder.
- 11. (original) The method of claim 9, wherein the providing step includes providing the vias with solder-wettable pads connected to the solder-wettable material of the through-holes.

Appl. No. 10/695,187 Amdt. Ditted March 20, 2006 Reply to Office Action of Junuary 18, 2006 Docket No. IS01207AP Customer No. 22917

- 12. (original) The method of claim 9, wherein the wicking step includes applying a vacuum to the vias to assist wicking of the solder into the vias and leaving a residual solder amount on the ball-grid array portion of the circuit substrate sufficient to solder a ball-grid array package thereon.
- 13. (original) The method of claim 12, wherein the providing step includes providing the vias with solder-wettable pads connected to the through-holes, and wherein the pads and the through-holes are configured to leave a predetermined amount of solder on the circuit substrate after the wicking and lifting steps.
- 14. (original) The method of claim 9, further comprising the step of applying flux to the sacrificial circuit substrate.
- 15. (withdrawn) A system for the vertical removal of excess solder from a circuit substrate, the system comprising:
  - a heat source for heating the excess solder to a liquidous state; and
  - a sacrificial circuit substrate with a plurality of vias, a portion of each via having a solderwettable material disposed thereon, the vias configured to align with a plurality of excess solder bumps on the circuit substrate, the plurality of vias of the sacrificial circuit substrate to be located in vertical proximity to the excess solder of the circuit substrate so as to wick the excess solder vertically into the vias of the sacrificial circuit substrate.
- 16. (withdrawn) The system of claim 15, wherein the circuit substrate is flexible and the sacrificial circuit substrate is rigid.
- 17. (withdrawn) The system of claim 15, wherein the heat source is a hot gas source to reflow the excess solder.
- 18. (withdrawn) The system of claim 15, wherein the vias of the sacrificial circuit substrate includes through-holes that are plated with a solder-wettable material and pads of solder-wettable material connected thereto.

Appl. No. 10/695,187 Amdt. Dated March 20, 2006 Reply to Office Action of Junuary 18, 2006 Docket No. IS01207AP Customer No. 22917

- 19. (withdrawn) The system of claim 18, further comprising a vacuum source, wherein the vacuum source is applied to the vias through-holes opposite the excess solder liquid to assist wicking of the solder into the vias, the vacuum source and vias being configured to leave a residual amount of solder on the circuit substrate after the wicking of the excess solder into the vias.
- 20. (withdrawn) The system of claim 15, further comprising flux applied to the sacrificial circuit substrate.